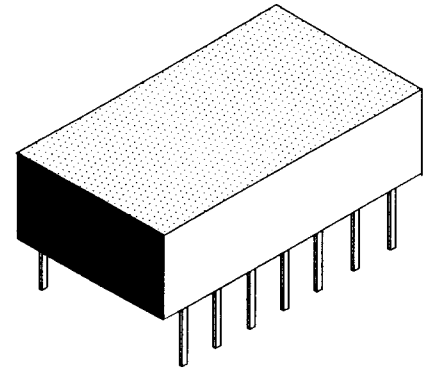


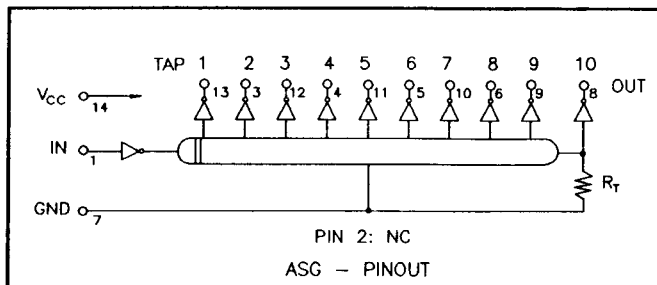
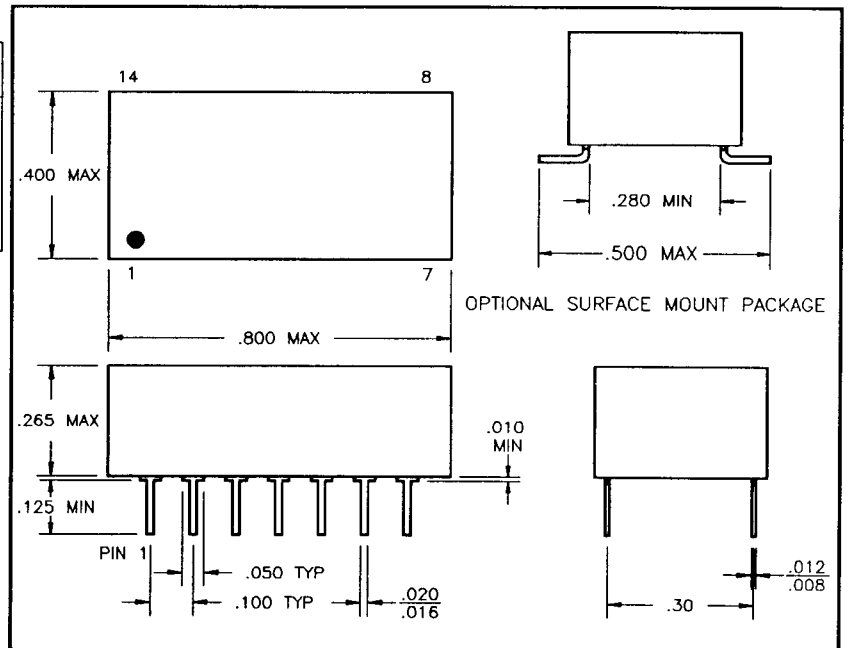
DC Electrical Characteristics		Test Condition	Min	Max	Unit
Parameter					
V _{OH}	High-Level Output Voltage	V _{CC} =min. V _L =max. I _{OH} =max	2.7		V
V _{OL}	Low Level Output Voltage	V _{CC} =min. V _{IH} =min I _{OL} =max		0.5	V
V _{IK}	Input Clamp Voltage	V _{CC} =min. I _I =-18mA		-1.2	V
I _{IH}	High-Level Input Current	V _{CC} =max. V _{IN} =2.7V		50	uA
I _{IL}	Low-Level Input Current	V _{CC} =max. V _{IN} =5.25V		1.0	mA
I _{OS}	Short Circuit Output Current	V _{CC} =max. V _{IN} =0.5V		-2	mA
		V _{CC} =max. V _{OUT} =0	-40	-100	mA
IC _{CH}	High-Level Supply Current	(One output at a time) V _{CC} =max. V _{IN} =OPEN		150	mA
IC _{CL}	Low-Level Supply Current	V _{CC} =max. V _{IN} =0		150	mA
T _{RO}	Output Rise Time	T _d ≤ 500nS(0.75 to 2.4 Volts)		4	nS
		T _d > 500nS		5	nS
N _H	Fanout High-Level Output	V _{CC} =max. V _{OH} =2.7V		20	TTL LOAD
N _L	Fanout Low-Level Output	V _{CC} =max. V _{OH} =0.5V		10	TTL LOAD



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-level Input voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW	Pulse Width of Total Delay	40		%
T _A	Operating Free-Air Temperature	0	+70	°C

Input Pulse Test Conditions @ 25°C		Unit
E _{IN}	Pulse Input Voltage	3.2 Volts
PW	Pulse Width % of Total Delay	110 %
T _{RI}	Pulse Rise Time(0.75 - 2.4 Volts)	2.0 nS
PRR	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0 MHz
	Pulse Repetition Rate @ T _d > 200 nS	100 KHz
V _{CC}	Supply Voltage	5.0 Volts

ADD "S" SUFFIX TO P/N, TO ORDER SURFACE MOUNT PACKAGE.



TOTAL DELAYS nS ± 5%	TAP DELAYS ± 2 nS OR 5%	POLARA P/N ASG - PINOUT
50	5	ASG-0050
100	10	ASG-0100
150	15	ASG-0150
200	20	ASG-0200
250	25	ASG-0250
300	30	ASG-0300
500	50	ASG-0500

This unit uses a custom designed, hermetically sealed ceramic semiconductor package that meets MIL-STD-38510, Paragraph 3.5.1.